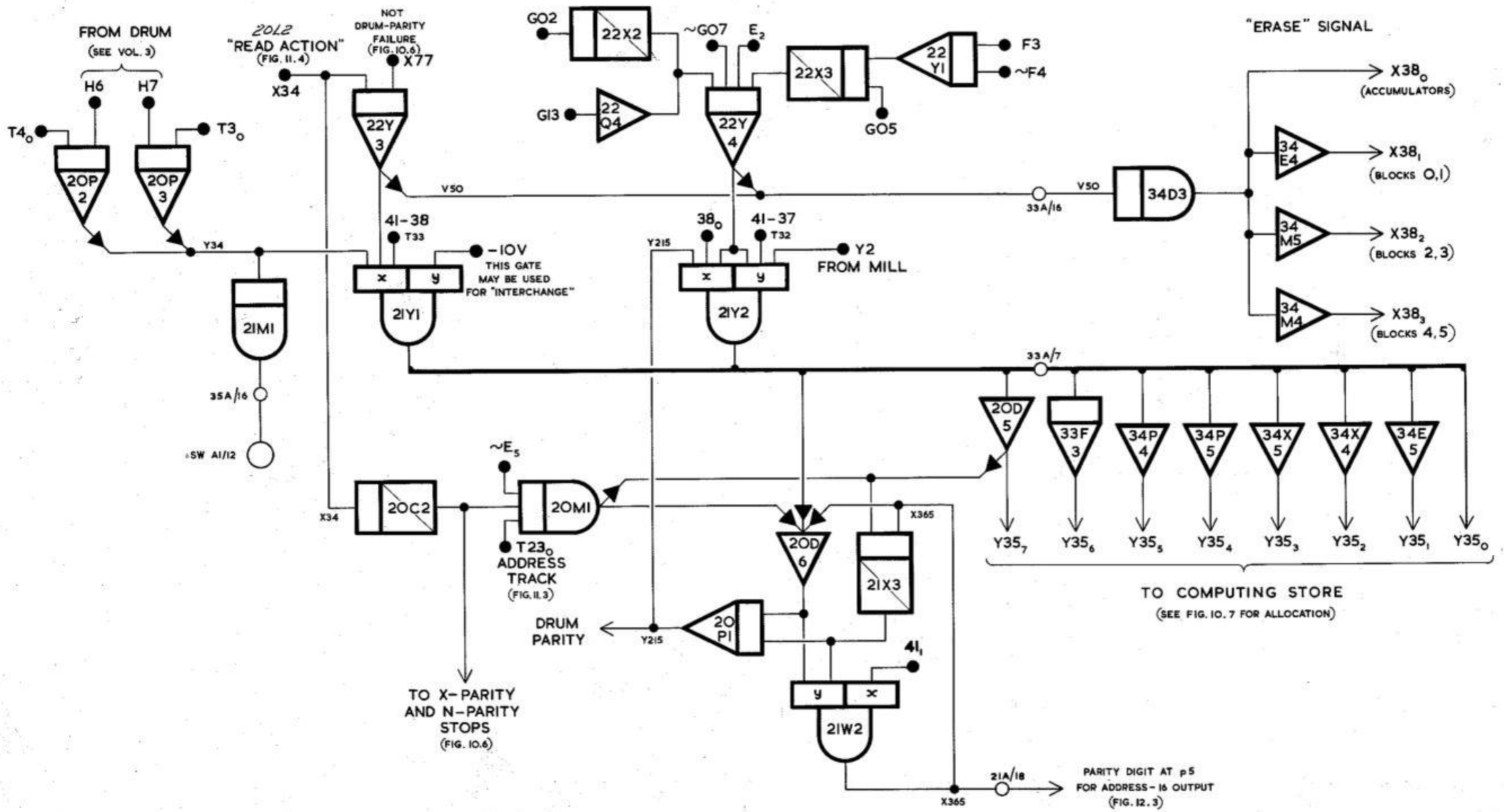

FIG. 10.5 INPUT BUS