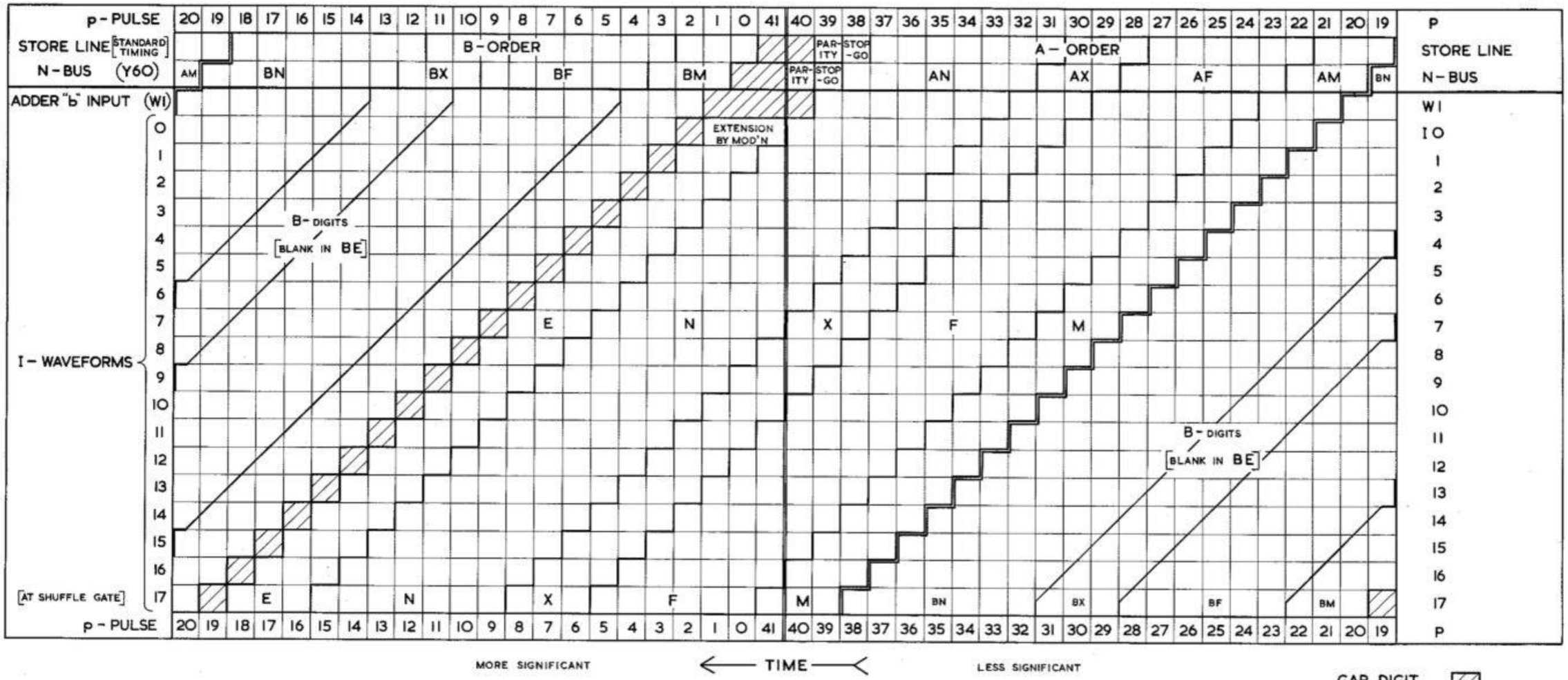

THIS DIAGRAM SHOWS HOW THE DIGITS OF AN ORDER ARE PARALLELED ON THE ORDER REGISTER DELAY CHAIN.

THE CURRENT ORDER (A BEFORE SHUFFLE ; B AFTER SHUFFLE) LIES BETWEEN THE ROW OF BLANKS AND THE DOUBLE STEPPED LINE.

DIGITS IN THE LESS SIGNIFICANT HALF-WORD ARE NOT SENSED ; THE B-ORDER GROUPS IN THIS POSITION ARE ONLY SUGGESTED.

IN BE THE ORDER NUMBER APPEARS IN PLACE OF THE LEAST SIGNIFICANT 6 N-DIGITS, ALL OTHER DIGITS BEING ZERO.

NOTE THAT THE ORDER-PAIR APPEARS ON THE N-BUS IN CD ONLY, BUT IS SHOWN CONTINUOUSLY TO PRESERVE THE SEQUENCE.

FIG. 6.3 THE ORDER REGISTER (INTERNAL)

SEE ALSO TIMING CHART 5