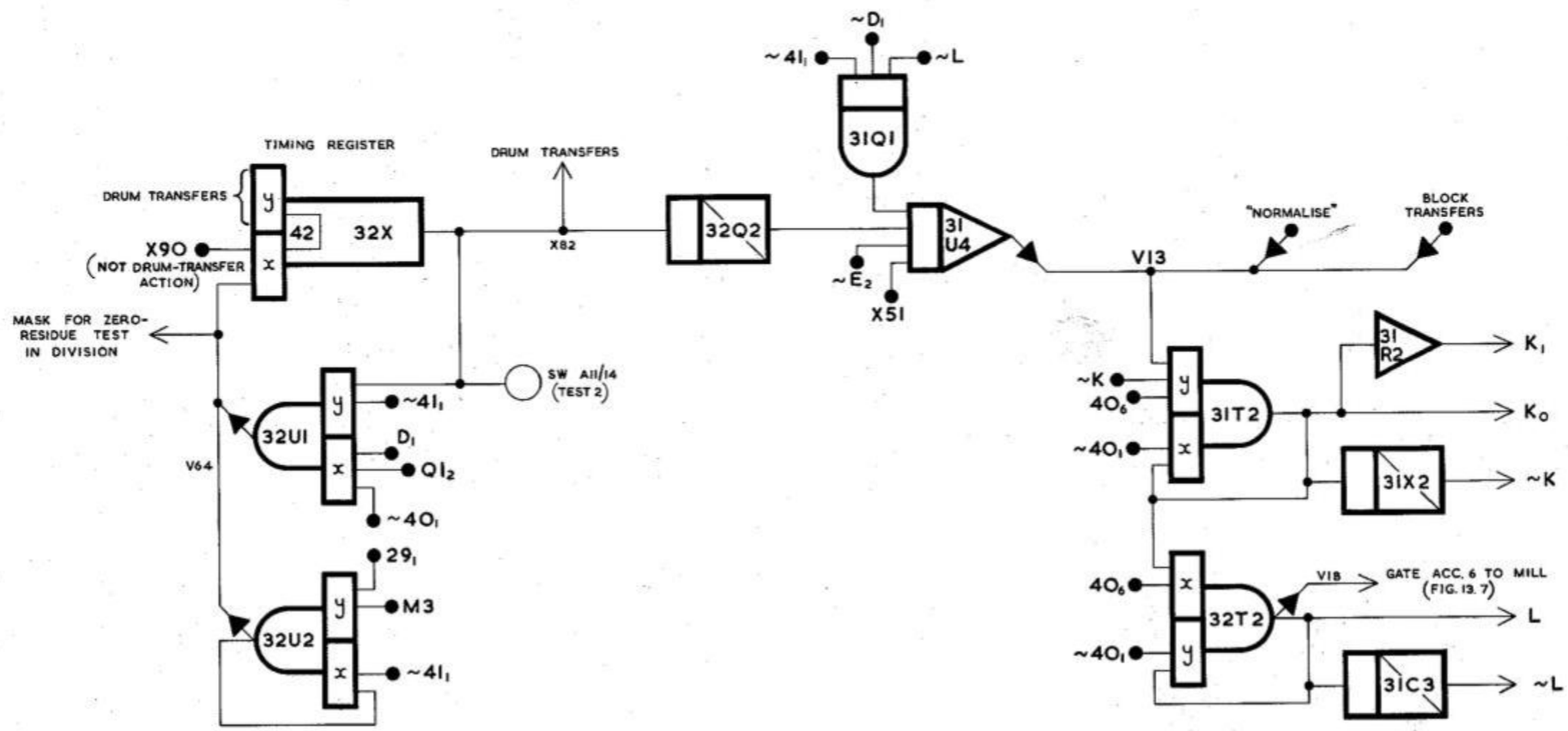

FIG. 13. 2 TIMING CONTROL FOR MULTIPLIER – DIVIDER